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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Application No.: 10/044,487

§ Examiner: Huisman, David J.

Filed: January 11, 2002

§ Group/Art Unit: 2183
§ Atty. Dkt. No: 5181-37601

Inventor(s):

Bodo K. Parady

Title: LOAD ADDRESS
PREDICTION IN A
MULTITHREADED
MICROPROCESSOR

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Rory D. Rankin

Printed Name

Signature

11 | 3 | 04
Date

RESPONSE TO OFFICE ACTION OF
SEPTEMBER 30, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of September 30, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.

IN THE TITLE

Please replace the title with the following new title:

--Load Address Prediction in a Multithreaded Microprocessor--